. . , . ,

FIG. 1A

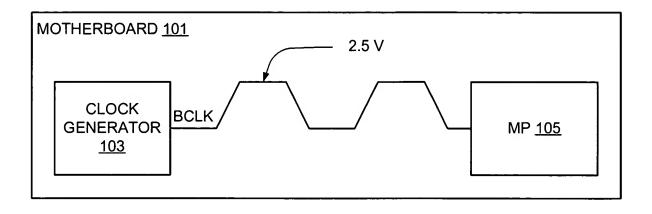


FIG. 1B

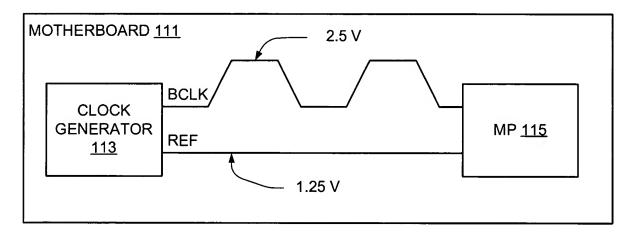


FIG. 1C

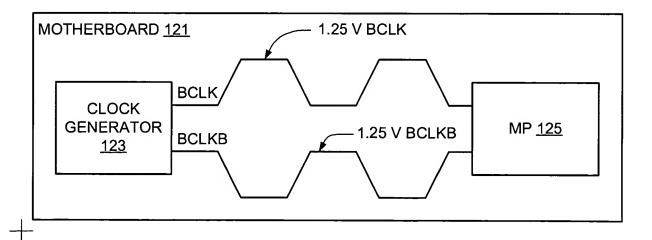


FIG. 2

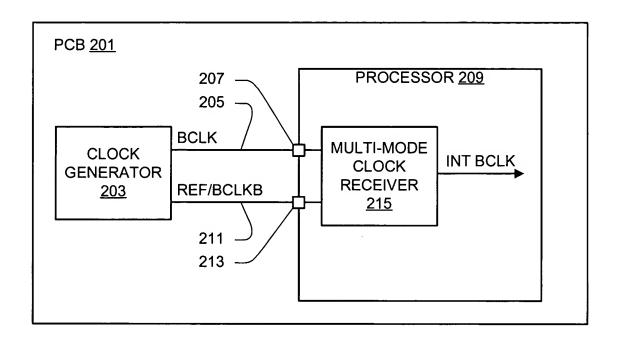
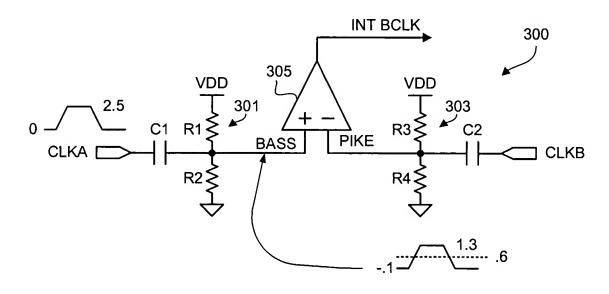


FIG. 3



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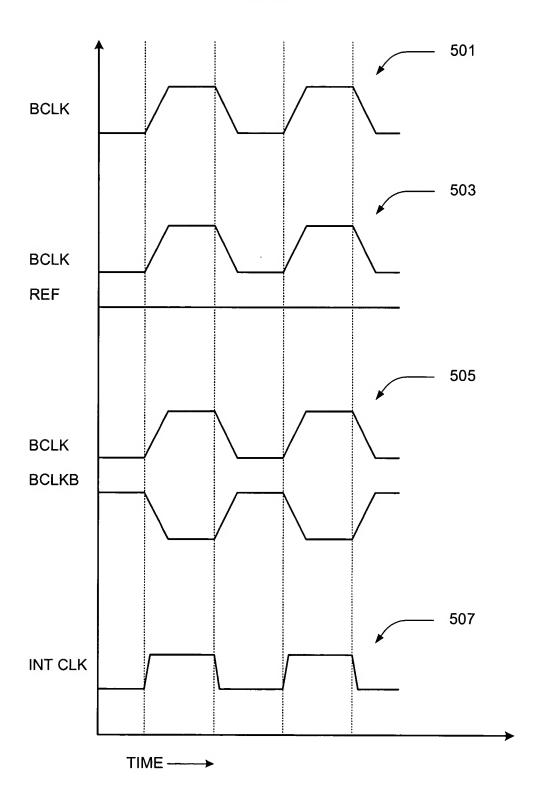
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FIG. 4 400 VDD VDD 405 CNT P6 407 401 ->INT BCLK C1 403 VDD VDD CLKA CLKB C2 PIKE N₁ N5 N7 N2 BASS N3 N6 N8

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FIG. 5



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